

2616

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

HAYASHI et al.

Application Number: 09/976,052

Filed: October 15, 2001

**For: LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR
INTEGRATED CIRCUIT**

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

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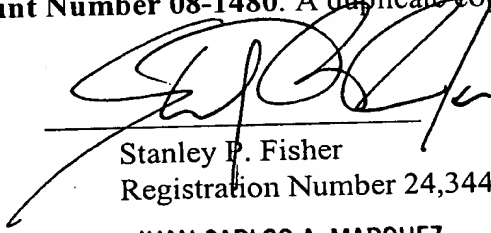
LETTER

Sir:

The below-identified communications are submitted in the above-captioned application or proceeding:

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The Commissioner is hereby authorized to charge payment of any fees associated with this communication, including fees under 37 C.F.R. § 1.16 and 1.17 or credit any overpayment to **Deposit Account Number 08-1480**. A duplicate copy of this sheet is attached.


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